



SIZE	QTY	SYM	PLATED	TOL
0.012	551	+	YES	+/-0.003"
0.031	6	◇	YES	+/-0.003"
0.065	4	⊗	YES	+/-0.003"
0.008	4	□	YES	+/-0.003"
0.07	2	×	NO	+/-0.003"

Diagram illustrating the cross-section of a 4-layer PCB structure. The total thickness is .062". The layers are labeled as follows:

- Microstrip (.0028")
- Core Material (.018")
- Microstrip (.0014")
- Prepreg (.0014")
- Core Material (.018")
- Microstrip (.0028")

The diagram shows a total of 4 layers, with the top layer being Microstrip and the bottom layer being Microstrip. The layers are stacked in the following order from top to bottom: Microstrip, Core Material, Microstrip, Prepreg, Core Material, and Microstrip.

1. ARTWORK P/N DEMO CIRCUIT 1591A
2. FAB PER IPC-A-600.
3. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4.
2 OZ. COPPER FINISH ON OUTER LAYERS.
1 OZ. COPPER FINISH ON INNER LAYERS.
THICKNESS .062 +/- .006 TOTAL OF 4 LAYERS.
FLAMMABILITY RATING: 94 V-0 MINIMUM .
4. SIZE: DIMENSIONS AND TOLERANCES SHOWN.
- 5 DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, .001 INCH THICK MIN. ALL
HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/- .003"
INCH IN RELATION TO CENTER
6. FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN.

GOLD IMMERSION BOTH SIDES.

7. SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
8. DO NOT ALTER ARTWORK e.g. TO ADD LOGO, DATE CODE AND ELECTRICAL STAMP.
9. CONTROLLED 50 OHM AND 100 OHM IMPEDANCE (AT 10GHz FREQ.) FOR:
LAYER 1: 30 MIL TRACE WIDTH'S REQUIRE 50 OHMS CONTROLLED IMPEDANCE.
LAYER 1: 10.5 MIL TRACE WIDTH'S REQUIRE 100 OHMS CONTROLLED IMPEDANCE.
- (10) SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC
CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

1591a.pcb - Thu Jun 24 11:13:30 2010